Two-way Set Associative Cache - 100

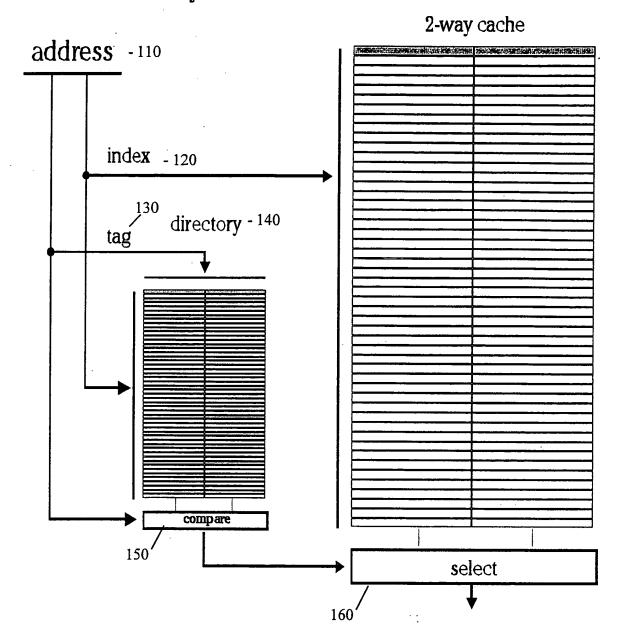


FIG. 1 PRIOR ART

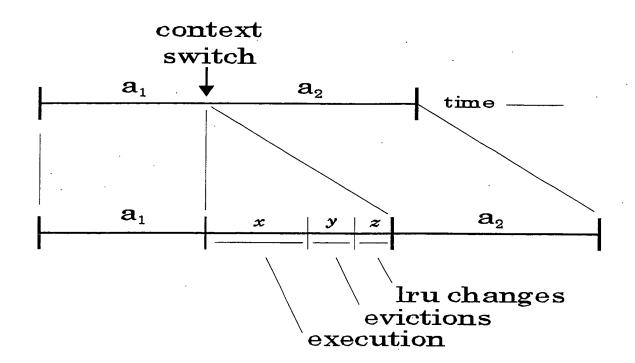


FIG. 2

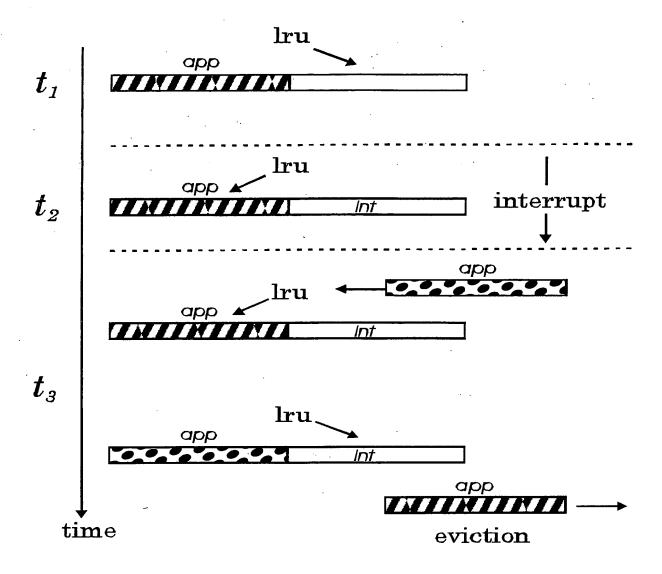
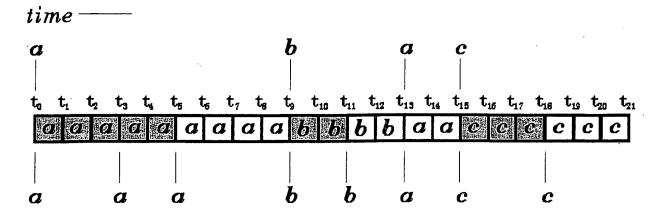


FIG. 3

blocks inserted into frame



blocks accessed

FIG. 4

Live Cache Frames vs. Time (AMR)

Live Cache Frames vs. Time (AMR)

Live Cache Frames vs. Time (AMR)

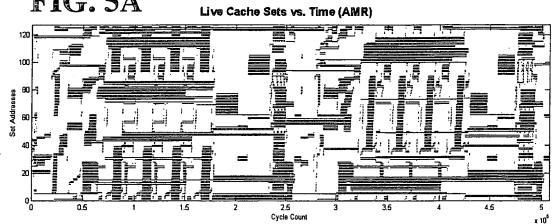


FIG. 5B

Sets containing a live frame at any time during an application's execution.

Sets occupied by an interrupt at any time during its execution.

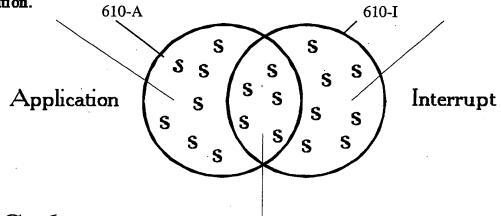


FIG. 6 Sets in the intersection may experience the eviction of a block from a live frame.

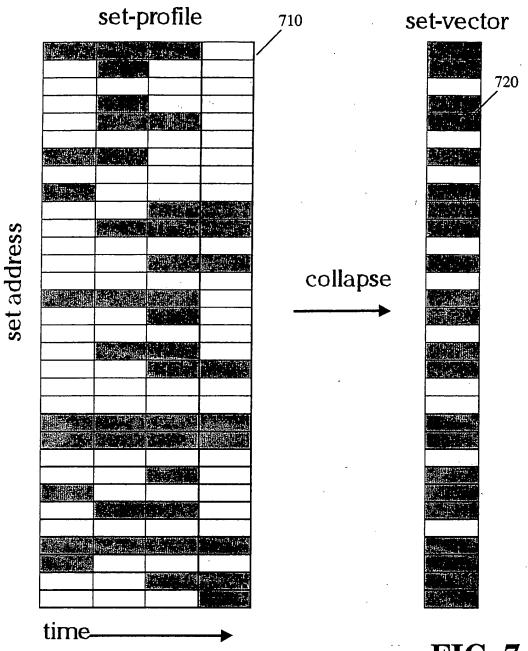
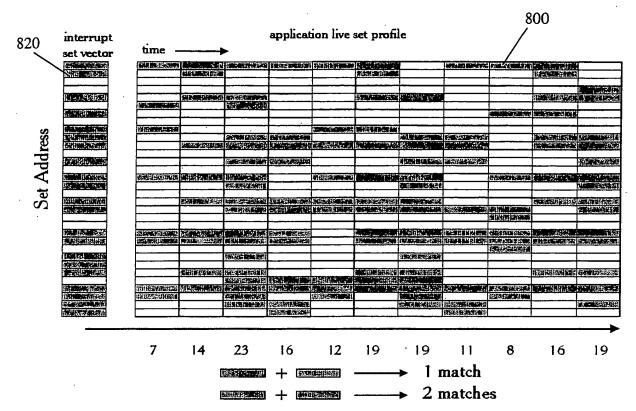


FIG. 7



Sweep the vector across the profile to find the maximum number of matches.

FIG. 8

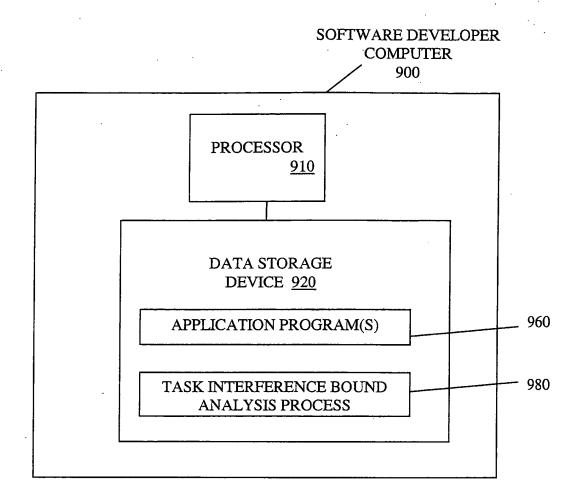


FIG. 9

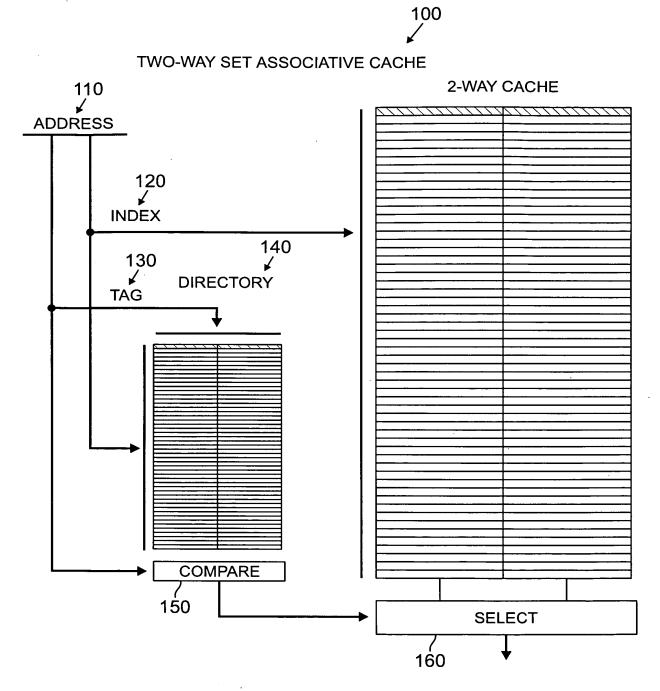
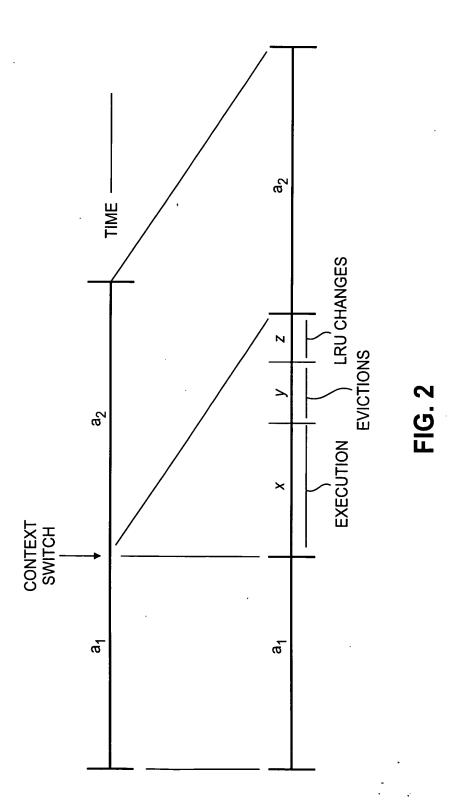


FIG. 1 (PRIOR ART)



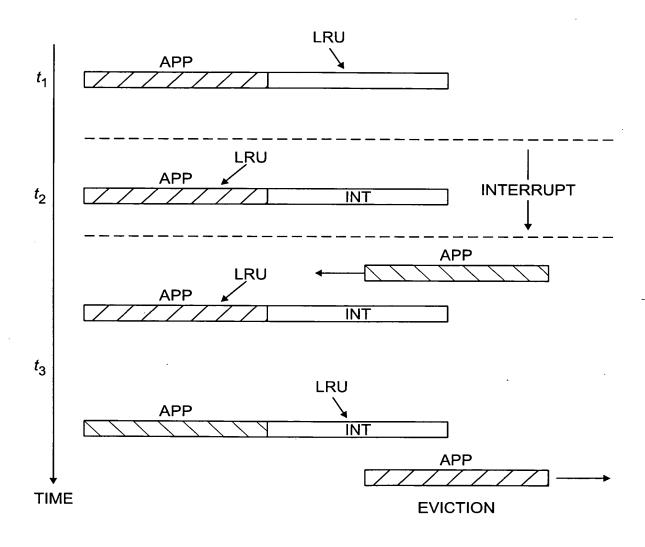


FIG. 3

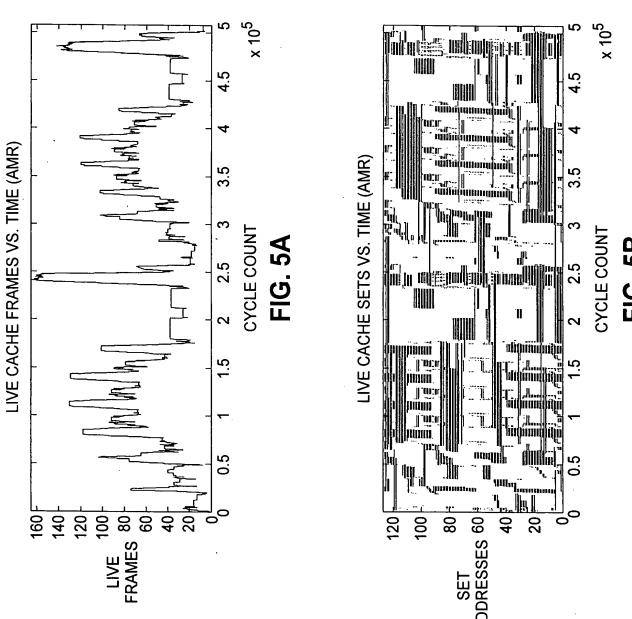
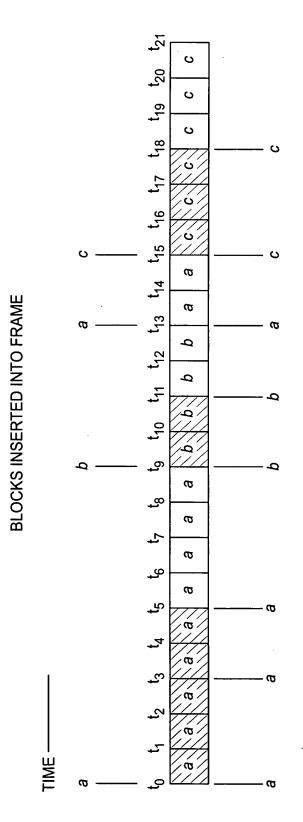


FIG. 5B



.

BLOCKS ACCESSED

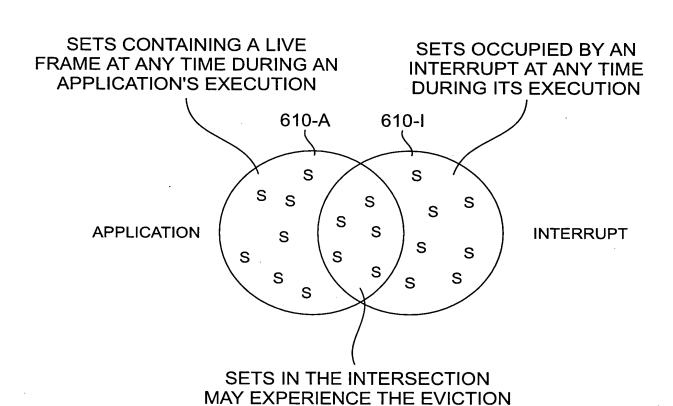


FIG. 6

OF A BLOCK FROM A LIVE FRAME

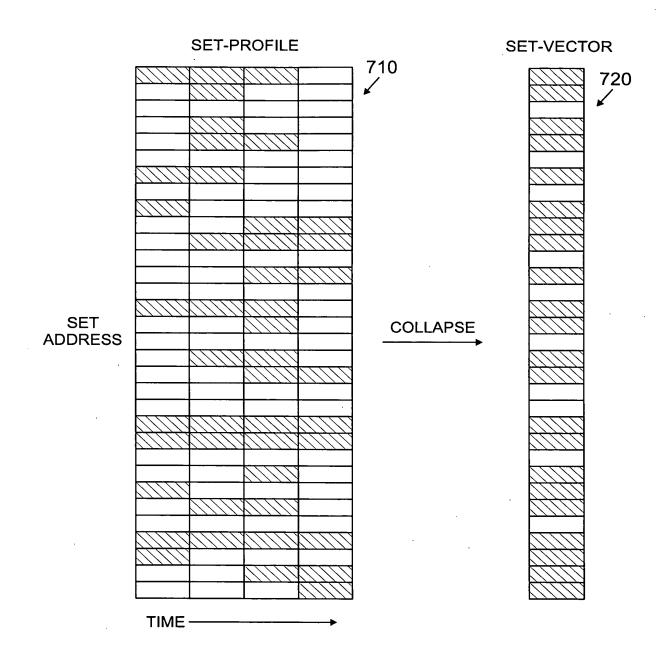
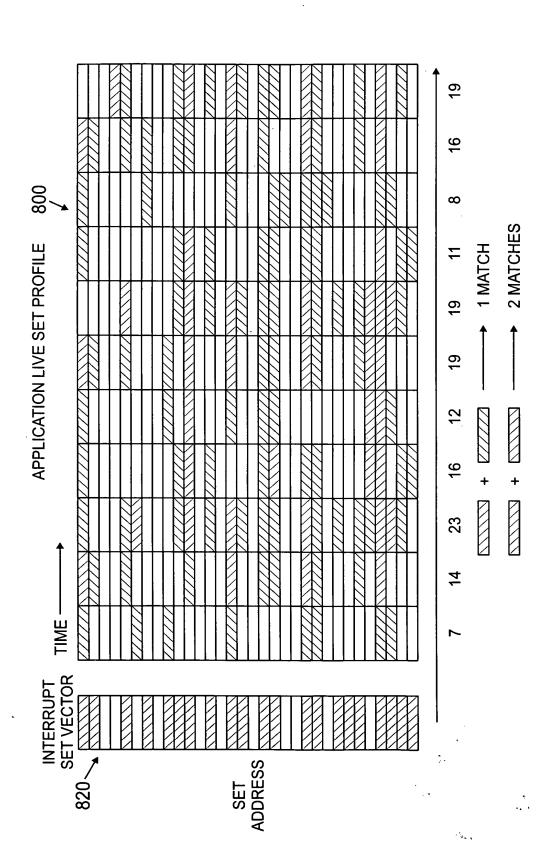


FIG. 7



SWEEP THE VECTOR ACROSS THE PROFILE TO FIND THE MAXIMUM NUMBER OF MATCHES

FIG. 8

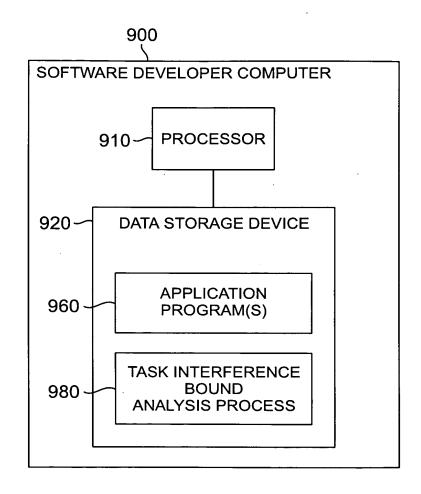


FIG. 9